

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: KAJIHARA, et al.
Serial No.: 09/989,242
Filed: November 21, 2001
For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO
Group: 2815
Examiner: Jasmine Jhihan B Clark
Conf. No.: 9603

STATEMENT OF SUBSTANCE OF INTERVIEW

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 12, 2010

Sir:

The following statement of the substance of the personal interview conducted between the Examiner; SPE and the undersigned on September 2, 2010 is being submitted in reply to the requirement in the Interview Summary dated September 10, 2010.

The Examiner's statement of the substance of the interview in the Interview Summary is complete and correct.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

/Paul J. Skwierawski/
Paul J. Skwierawski
Registration No. 32,173

PJS/slk
Tel.: 703-312-6600